

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under Reexamination 10/734,457 JOHNSON ET AL.	
		Examiner Qing Chen	Art Unit 2191	Page 1 of 1

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*	B	US-5,809,450	09-1998	Chrysos et al.	702/186
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NON-PATENT DOCUMENTS

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	V	Mowry, Todd C., "Tolerating Latency in Multiprocessors through Compiler-Inserted Prefetching," 1998, ACM, pg. 55-92.		
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.